Appl. No.: 09/613,340

Amdt. dated: December 18, 2003 Reply to Office Action of July 18, 2003

Amendments to the Drawings:

The attached sheets of drawings includes changes to Figs. 1, 2, and 5. These sheets, which includes Figs. 1, 2, and 5, replace the original sheets including Figs. 1, 2, and 5. In Figs. 1 and 2 the DVD 22 has been changed to DVD 19, and in Fig. 5 element 70 has been changed to element 71.

In the Office Action mailed July 18, 2003, the Examiner objected to the drawings of the present application. The drawings were objected to as failing to comply with 37CFR 1.84(p)(4) because reference character "18" had been used to designate both the upper and lower bus in Fig. 1. Also in Fig. 1, reference character "22" points to a DVD stack and SATCOM2. Further, in Fig. 5, reference character "70" points to the HDD and the NID. In addition, the Examiner objected to the drawings as failing to comply with 37 CFR 1.84(p)(5) for failing to mention the reference sign "64" in the description. The Examiner also objected to Fig. 1 because the top bus 18 separates the processors from the displays contrary to claim 1, and objected to Fig. 2 on the grounds that the displays (12) are shown and not the processors/memory (14) contrary to claim 1.

In response, the Applicant has amended Figs. 1, 2, and 5 and the specification of the present application. The Applicants respectfully submit that no new matter has been added. In addition, the Applicant respectfully submit that the specification of the present application states:

"[I]n the particular exemplary embodiment of FIG. 1, the signal bus 18 is depicted as lying between the multiplicity of DPUs and the multiplicity of display units, as well as between the multiplicity of DPUs and a communication management unit (CMU) 20. The particular configuration of the embodiment of FIG. 1 is solely for purposes of illustration and ease of explanation and is not intended to imply that there are two separate signal busses. Indeed, only a single network

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bus is contemplated by the present invention, with the network bus interconnecting the multiplicity of electronics packages with, for example, an aircraft communication management unit 20."

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With respect to the Examiner's objection to Fig. 2 on the grounds that the displays (12) are shown and not the processors/memory (14) contrary to claim 1, the Applicant respectfully directs the Examiner's attention to the specification of the present invention which states:

"Although depicted in the exemplary embodiment of FIG. 1 as being separate from the display unit 12, the DPU 14 might well be integrated with the display in a complete electronic subassembly that, together with a suitable housing, can be inserted into a suitable receptacle provided any convenient location on the rear side of a passenger seat."

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Attachment: Replacement Sheets for Figs. 1, 2, and 5
Annotated Sheet Showing Changes

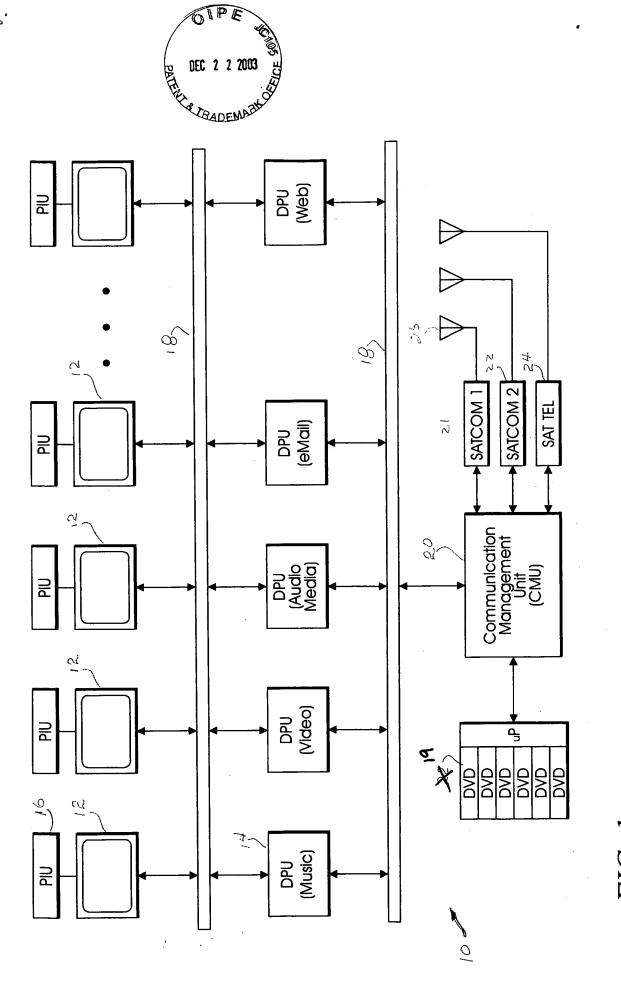
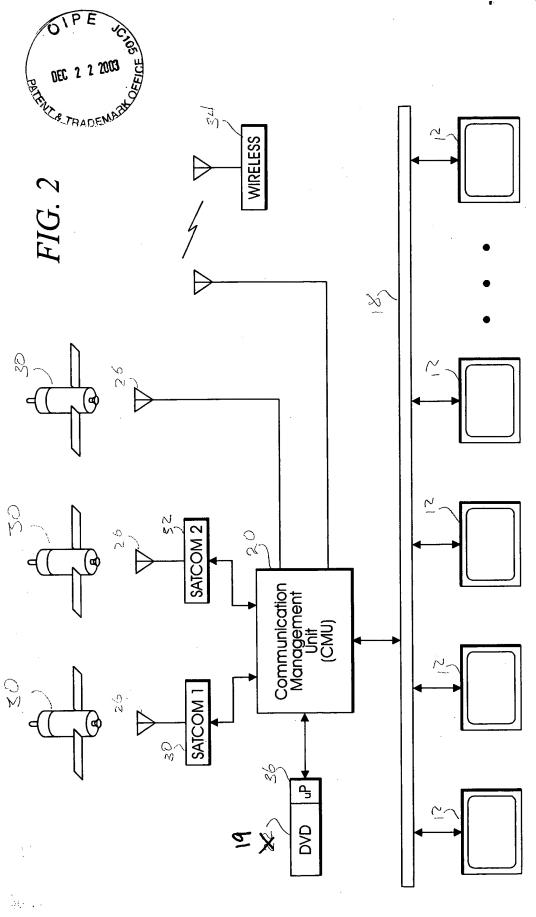


FIG. 1



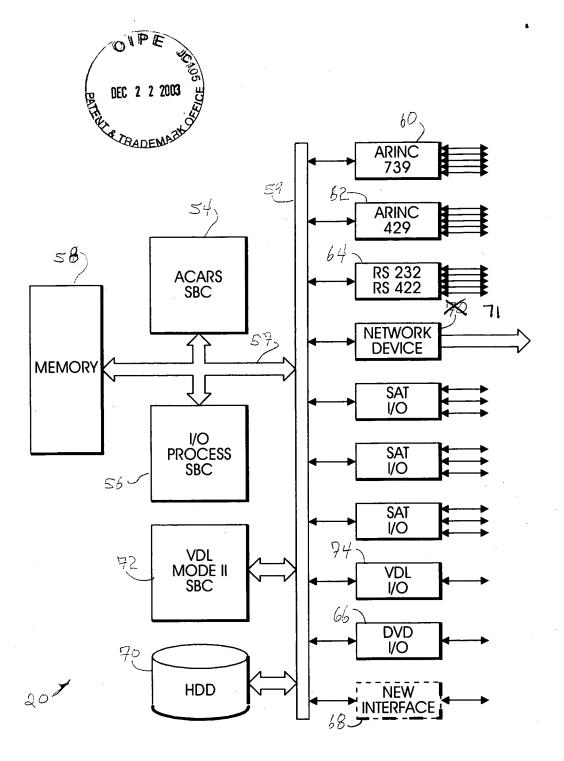


FIG. 5